

## **AMENDMENTS TO THE SPECIFICATION**

### **Abstract of the Disclosure**

A packet generation unit 47 divides a plurality of generated clocks to generate clocks with different frequencies, selects any of the frequencies, sets destination information and data depending on a selected clock rate and generates a data packet that stores the setting result. An input/output control unit 44 takes the data packet generated by the packet generation unit 47 and sends it to a program storage unit 42 or a data memory interface unit 45 according to the destination information. As a result, information can be internally processed in a predetermined frequency without depending on an outside clock. When the outside clock is slow, information can be internally processed in a higher-rate frequency. On the contrary, when the outside clock is too fast to internally process information, information can be internally processed in a lower-rate frequency to secure a processing time.